

SEMICONDUCTOR TECHNICAL DATA

KIA7019AP/AF/AT~ KIA7045AP/AF/AT

BIPOLAR LINEAR INTEGRATED CIRCUIT

VOLTAGE DETECTOR

Function of this IC is accurately resetting the system after detecting voltage at the time of switching power on and instantaneous power off in various CPU systems and other logic systems.

FEATURES

- · Current Consumption is Low. I_{CCL}=300 µA Typ. I_{CCH}=30 µA Typ.
- · Resetting Output Minimum Guarantee Voltage is Low 0.8V Typ.
- · Hysteresis Voltage is Provided. 50mV Typ.
- · Reset Signal Generation Starting Voltages:

KIA7019 1.9V Typ. KIA7033 3.3V Typ.

KIA7021 2.1V Typ. KIA7034 3.4V Typ.

KIA7023 2.3V Typ. KIA7035 3.5V Typ.

KIA7025 2.5V Typ. KIA7036 3.6V Typ.

KIA7027 2.7V Typ. KIA7039 3.9V Typ.

KIA7029 2.9V Typ. KIA7042 4.2V Typ.

KIA7031 3.1V Typ. KIA7045 4.5V Typ.

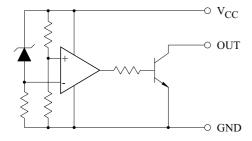
KIA7032 3.2V Typ.

· Taping Type is also Available.

APPLICATIONS

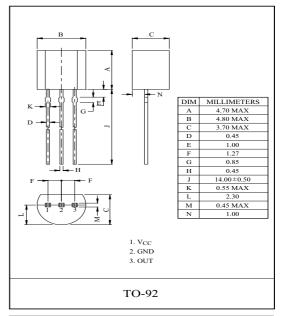
- (1) As Control Circuit of Battery-Backed Memory.
- (2) As Measure Against Erroneous Operations at Power ON-OFF.
- (3) As Measure Against System Runaway at Instantaneous Break of Power Supply etc.
- (4) As Resetting Function for the CPU-Mounted Equipment, such as Personal Computers, Printers, VTRs and so forth.

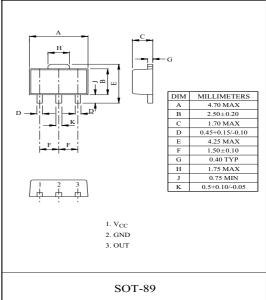
EQUIVALENT CIRCUIT

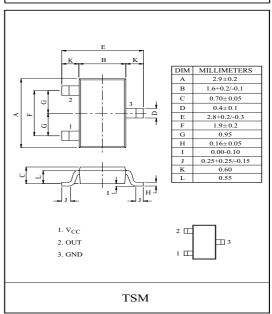


Marking

| Type No. | Marking | Type No. | Marking | Type No. | Marking |
|--------------|---------|--------------|---------|--------------|---------|
| KIA7019AF/AT | 6A | KIA7029AF/AT | 6F | KIA7035AF/AT | 6L |
| KIA7021AF/AT | 6B | KIA7031AF/AT | 6G | KIA7036AF/AT | 6M |
| KIA7023AF/AT | 6C | KIA7032AF/AT | 6Н | KIA7039AF/AT | 6N |
| KIA7025AF/AT | 6D | KIA7033AF/AT | 6J | KIA7042AF/AT | 6P |
| KIA7027AF/AT | 6E | KIA7034AF/AT | 6K | KIA7045AF/AT | 6R |







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MAXIMUM RATINGS (Ta=25 ℃)

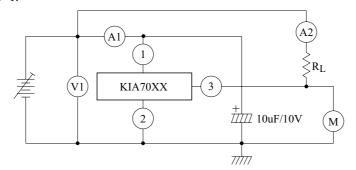
| CHARACTERI | SYMBOL | RATING | UNIT | | |
|--|-----------------|------------------|---------------|----|--|
| Supply Voltage | V _{CC} | -0.3 ~+15.0 | V | | |
| Power Dissipation (Package Limitation) | KIA7019AP ~45AP | | 400 | | |
| | KIA7019AF ~45AF | P_{D} | 500 | mW | |
| | KIA7019AT ~45AT | | 350 | | |
| Operating Temperature | T_{opr} | - 30 ∼+75 | $^{\circ}$ | | |
| Storage Temperature | T_{stg} | -55 ∼+150 | ${\mathbb C}$ | | |

ELECTRICAL CHARACTERISTICS (V_{CC}=5V, V_{EE}=GND, Ta=25 $^{\circ}$ C)

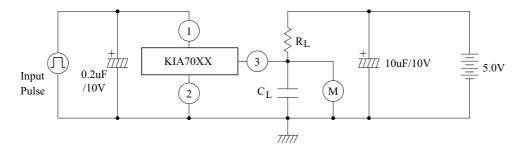
| CHARACTERISTIC | SYMBOL | TEST CIRCUIT | TEST CONDITION | | MIN. | TYP. | MAX. | UNIT |
|--|-----------------|-----------------|--|--|--|---|--|------|
| Detecting Voltage | V _S | 1 | $R_L=200 \Omega$ $V_{OL} \leq 0.4V$ | KIA7019 KIA7021 KIA7023 KIA7025 KIA7027 KIA7029 KIA7031 KIA7032 KIA7033 KIA7034 KIA7035 KIA7036 | 1.75 1.95 2.15 2.35 2.55 2.75 2.95 3.05 3.15 3.25 3.35 3.45 | 1.9 2.1 2.3 2.5 2.7 2.9 3.1 3.2 3.3 3.4 3.5 3.6 3.9 | 2.05 2.25 2.45 2.65 2.85 3.05 3.25 3.35 3.45 3.55 3.65 3.75 4.05 | V |
| | | | | KIA7042 KIA7045 | 4.05 4.35 | 4.2 4.5 | 4.35 4.65 | |
| Low-Level Output Voltage | V _{OL} | 1 | R _L =200 Ω | | - | - | 0.4 | V |
| Output Leakage Current | I _{OH} | 1 | V _{CC} =15V | | - | - | 0.1 | μA |
| Hysteresis Voltage | ⊿Vs | 1 | $R_L=200 \Omega$ | | 30 | 50 | 100 | mV |
| Detecting Voltage Temperature Coefficient | Vs/⊿T | 1 | R_L =200 Ω | | - | ±0.01 | - | %/°C |
| Circuit Current at on Time | IccL | 1 | V _{CC} =Vsmin0.05V | | - | 300 | 500 | μA |
| Circuit Current at off Time | IccH | 1 | V _{CC} =5.25V | | - | 30 | 50 | μA |
| Threshold Operating Voltage | Vopr | 1 | R_L =200 Ω , $V_{OL} \leq 0.4 V$ | | - | 0.8 | - | V |
| "L" Transmission Delay Time | tpHL | 2 | $R_L=1.0k \Omega$, $C_L=100pF$ | | - | 10 | - | μS |
| "H" Transmission Delay Time | tpLH | 2 | $R_L=1.0k \Omega$, $C_L=100pF$ | | - | 15 | - | μS |
| Output Current at on Time I | IoL I | 1 | V _{CC} =Vsmin0.05V, Tc=25 ℃ | | 20 | - | - | mA |
| Output Current at on Time II | IoL II | 1 | V _{CC} =Vsmin0.05V, Tc=-30 ~+75 ℃ | | 16 | - | - | mA |

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TEST CIRCUIT 1.



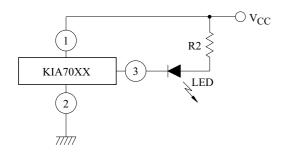
TEST CIRCUIT 2.

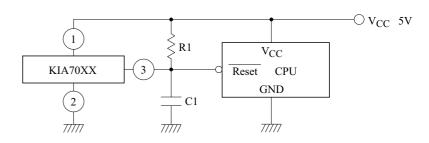


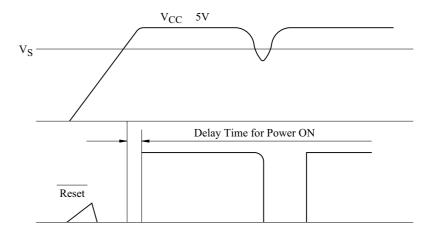
APPLICATION CIRCUIT

(1) BATTERY LOW INDICATOR

(2) CPU RESETTING







(NOTE)

- (1) Connecting of LED and R2 obtains a voltage drop indicator.
- (2) Connecting of C1 and selection of time constant with C1 and R1 set the power on delay time.

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PRECAUTION FOR USE

SOLDERING

Flat Package (SOT-89 Package)

Elements mounting styles of electronic devices are gaining in further diversification over recent years, and needs for components are all the more expanding in varieties. Especially, surface mounting is steadily penetrating into industrial segments as a world-wide popular technical trend. Although exposure to high temperature is inevitable during soldering we recommend limiting the soldering temperature to low levels as shown in figure for the sake of retaining inherent excellent reliability.

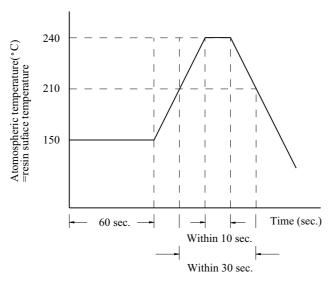


Fig 6

- (a) When employing solder reflow method
 - ① Atmospheric temperature around resin surfaces must be less than 240 °C, not exceeding the time length of 10 sec.
 - ② Recommend temperature profile
 - 3 Precautions on heating method

When resin in kept exposed to high temperature for a long time, device reliability may be marred.

Therefore, it is essential to complete soldering in the shortest time possible to prevent temperature of resin from rising.

- (b) When employing halogen lamps or infrared-ray heaters
 - When halogen lamps or infrared-ray heaters are used, avoid direct irradiation onto resin surfaces; such devices cause extensive localized temperature rise.
- * Please keep a reflow solder operating when SOT-89 package's soldering.